

08/242,749

1/15  
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9  
3.4. FIG. 3  
370 79

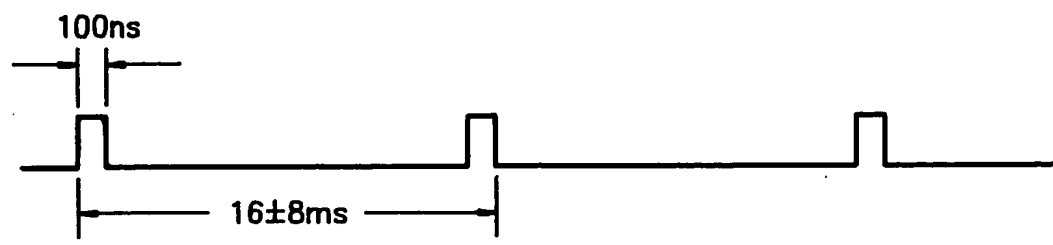


FIG. 1

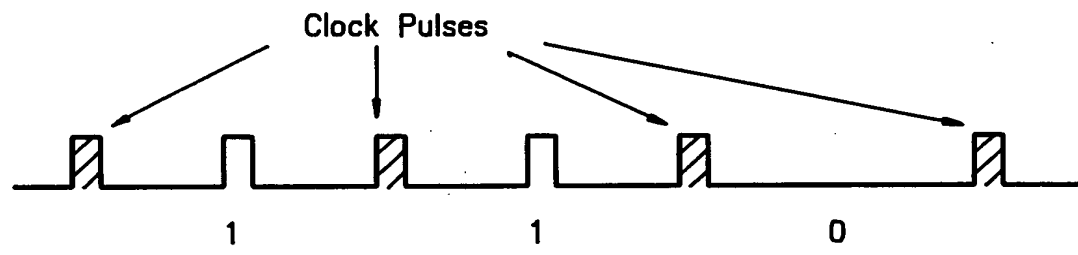


FIG. 2

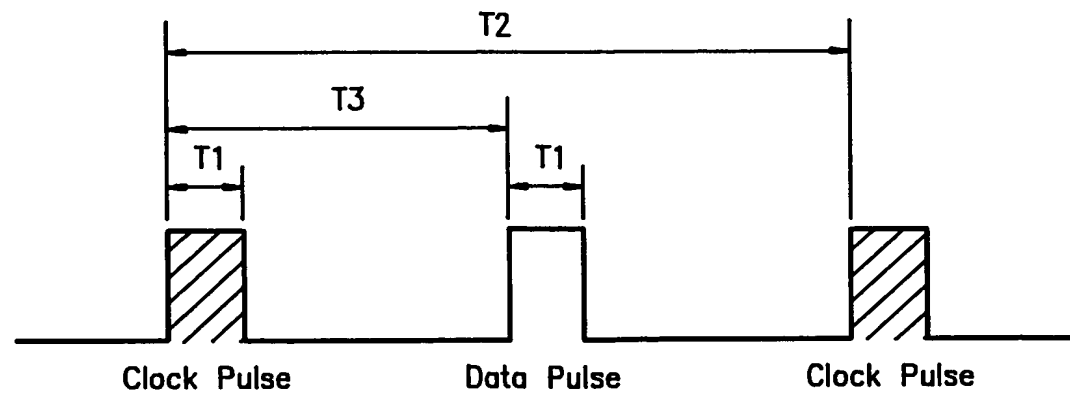


FIG. 3

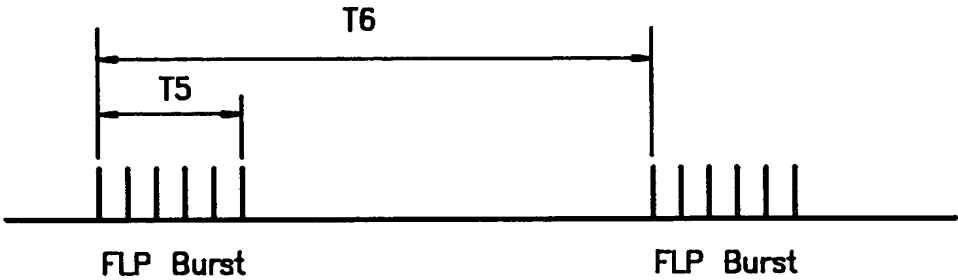


FIG. 4

#	Parameter	Min	Typ	Max	units
T1	Clock/Date Pulse Width		100		ns
T2	Clock Pulse to Clock Pulse		125		μs
T3	Clock Pulse to Data Pulse (Data=1)		62.5		μs
T4	Pulses in a Burst	17		33	#
T5	Burst Width		2		ms
T6	Burst to Burst	8	16	24	ms

FIG. 5

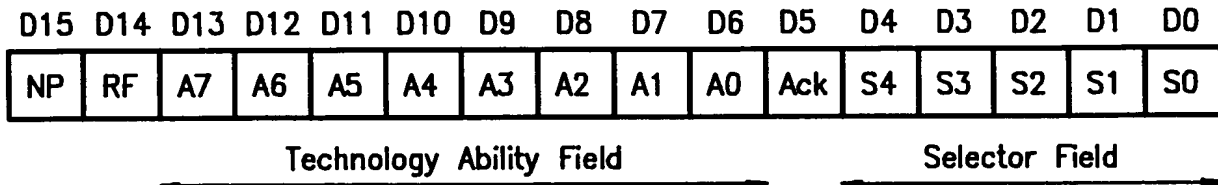


FIG. 6

D10	D9	D8	D7	D6	Description
0	0	0	0	1	10BASE-T
0	0	0	1	0	10BASE-T Full Duplex
0	0	1	0	0	100BASE-TX
0	1	0	0	0	100BASE-TX Full Duplex
1	0	0	0	0	100BASE-T4

FIG. 7

D10	D9	D8	D7	D6	Description
0	0	0	1	1	Regular and Full Duplex 10BASE-T
1	0	1	0	1	10BASE-T, 100BASE-TX, and 100BASE-T4 all supported in normal configurations.
0	0	1	0	1	10BASE-T and 100BASE-TX configurations both supported.
1	1	1	0	0	100BASE-TX,100BASE-T4,100BASE-TX Full Duplex supported.

FIG. 8

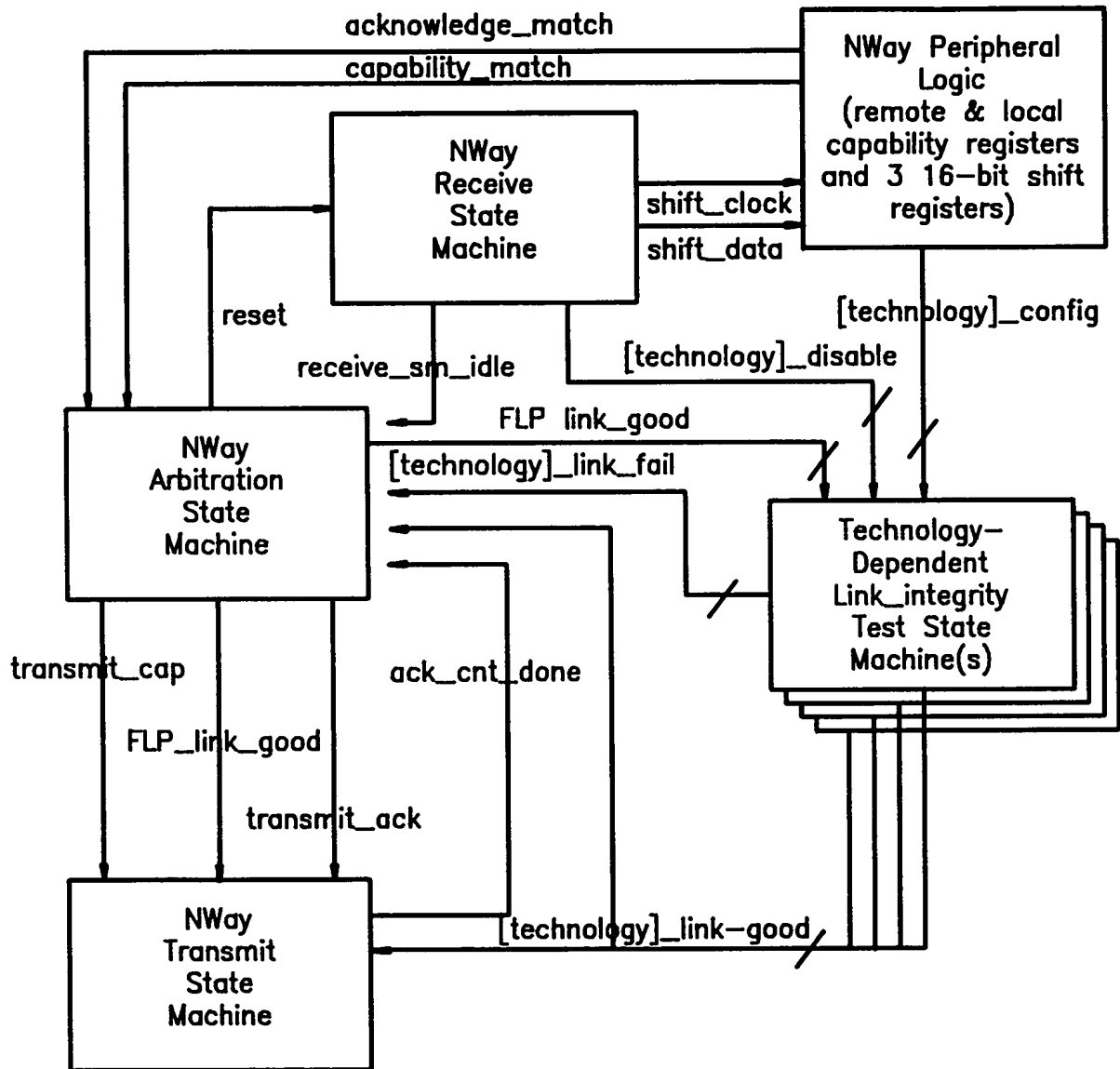
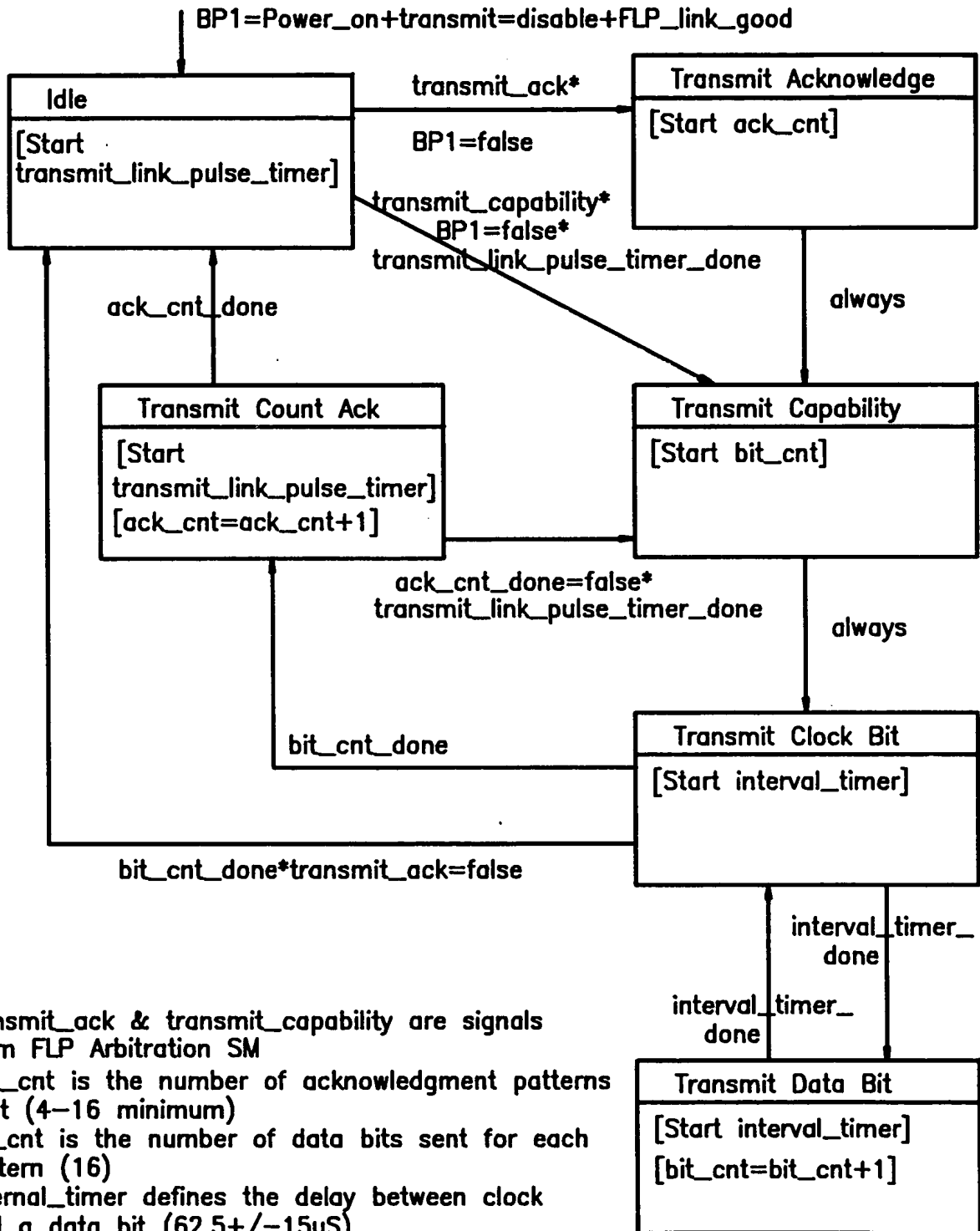


FIG. 9



- Note • Ability\_match is 3 consecutive data pattern match, ignoring acknowledgment bit.
- Acknowledge\_match is 3 consecutive data patterns having acknowledgment bit set.
  - ack\_cnt\_done is from FLP Transmit SM to indicate completion of sending a minimum of 4–16 acknowledge patterns.
  - optional remote sensing capability, refer to Management support section.



**Note:**

- $transmit\_ack$  &  $transmit\_capability$  are signals from FLP Arbitration SM
- $ack\_cnt$  is the number of acknowledgment patterns sent (4–16 minimum)
- $bit\_cnt$  is the number of data bits sent for each pattern (16)
- $interval\_timer$  defines the delay between clock and a data bit (62.5+/-15uS)
- $transmit\_link\_pulse\_timer$  defines the delay between two consecutive FLP bursts (14+/-8mS).

FIG. 11

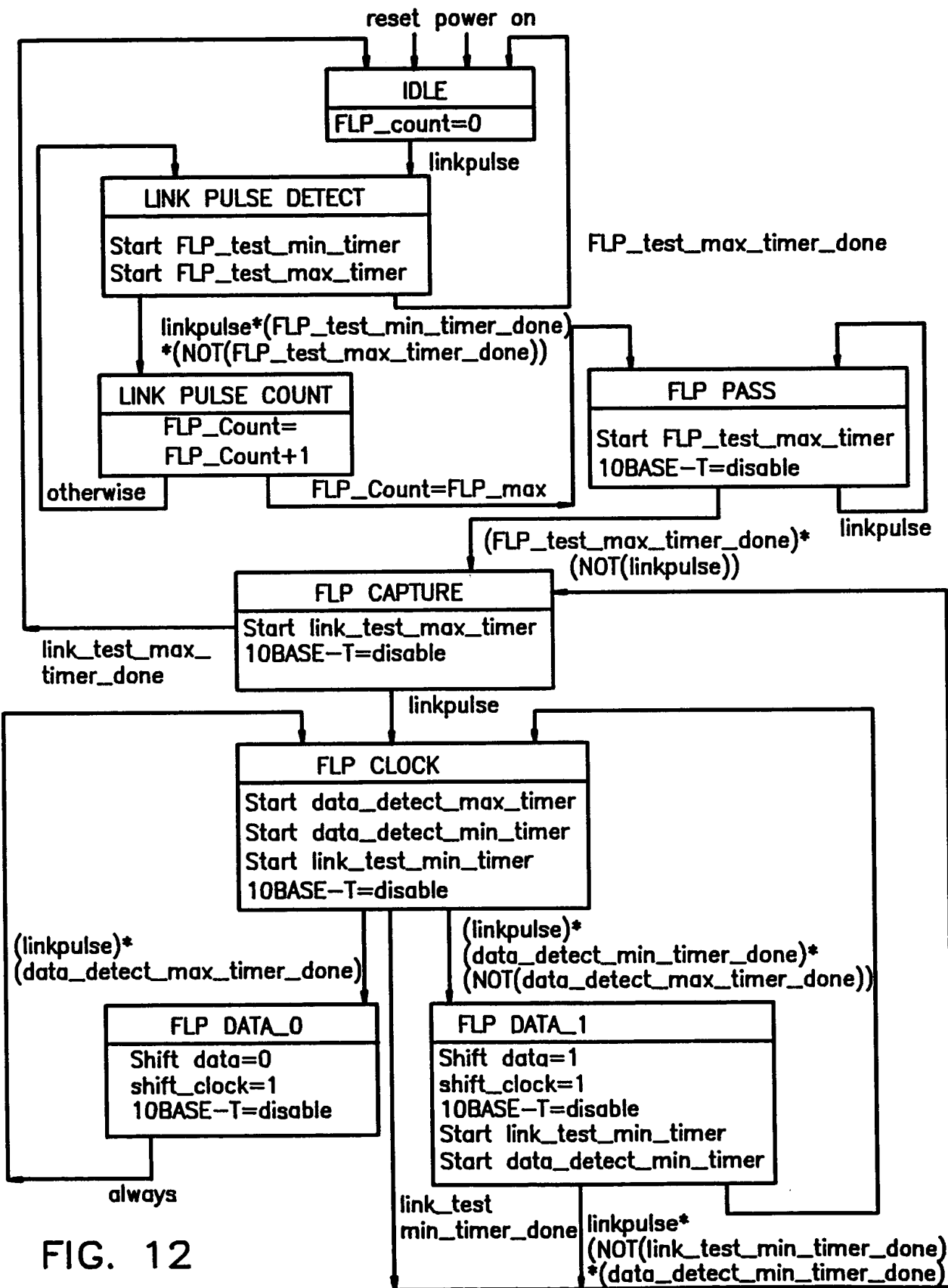


FIG. 12

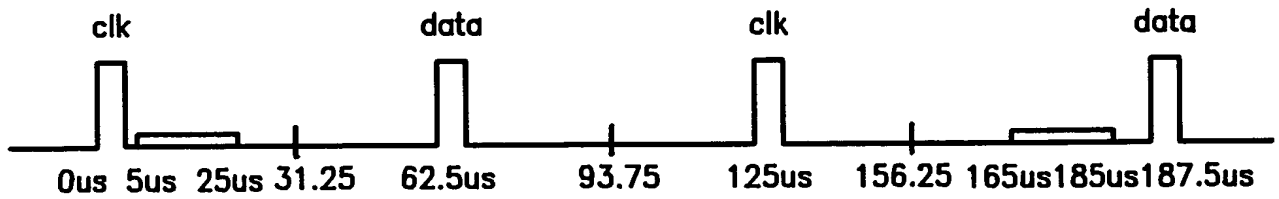


FIG. 13

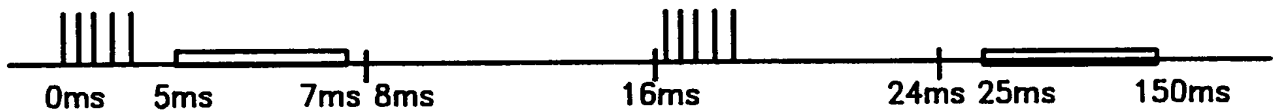


FIG. 14

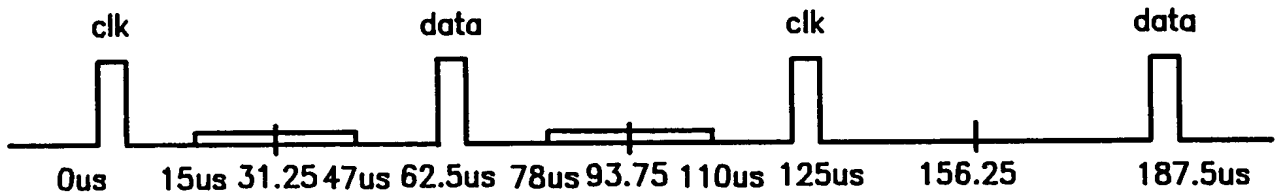


FIG. 15

Parameter	Min	Typ	Max	units
FLP_test_min_timer	5		25	us
FLP_test_max_timer	165		185	us
Link_test_min_timer	5		7	ms
Link_test_max_timer	50		150	ms
Data_detect_min_timer	15		47	us
Data_detect_max_timer	78		110	us

FIG. 16



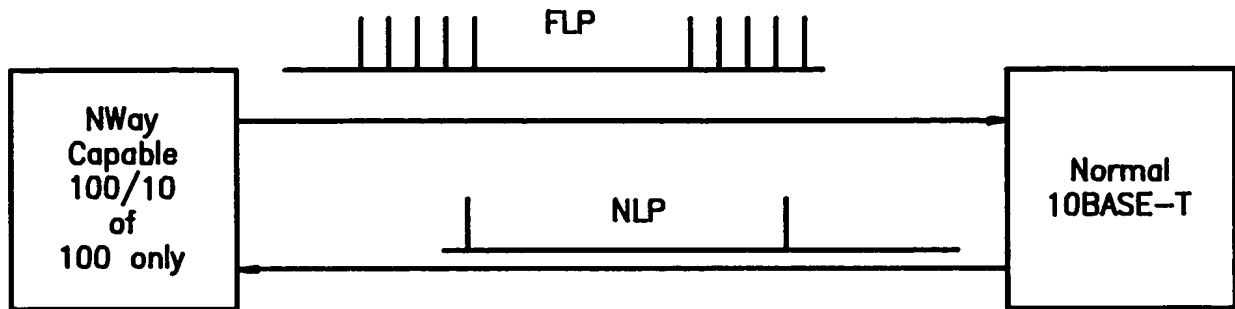


FIG. 17

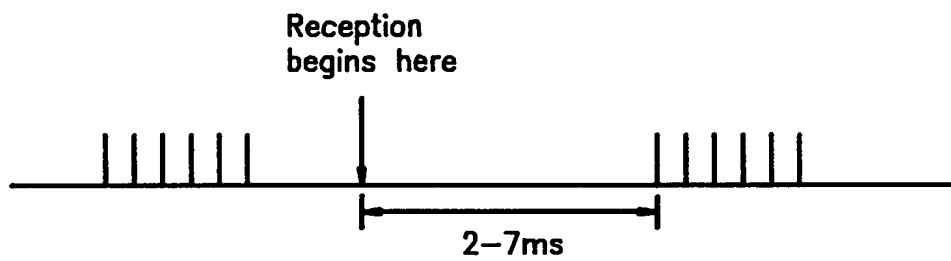


FIG. 18



FIG. 19

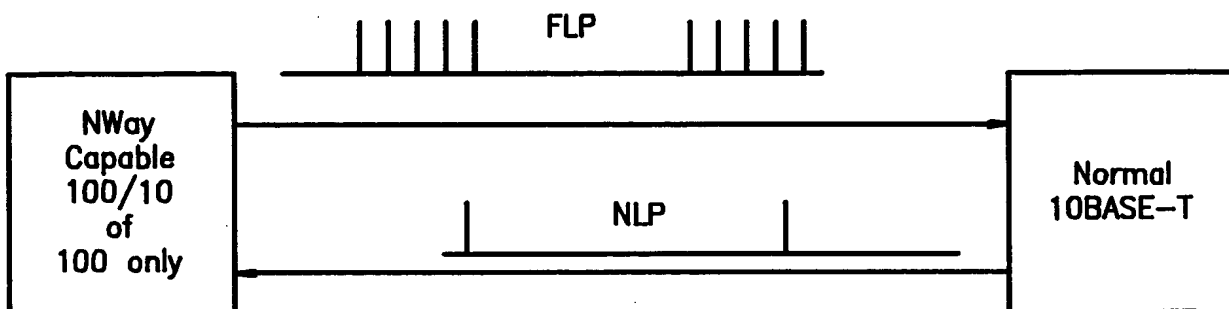


FIG. 20

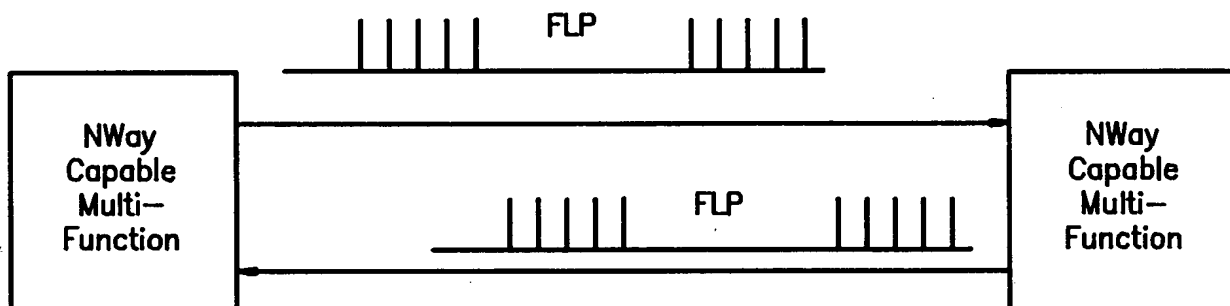


FIG. 21

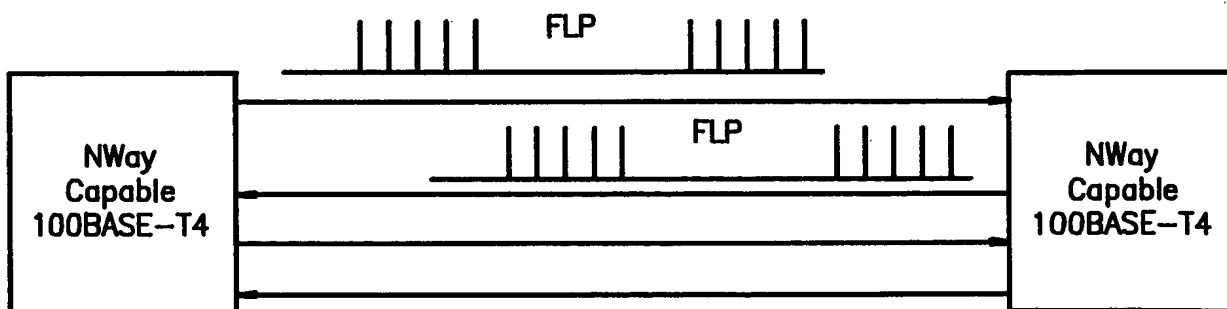


FIG. 22

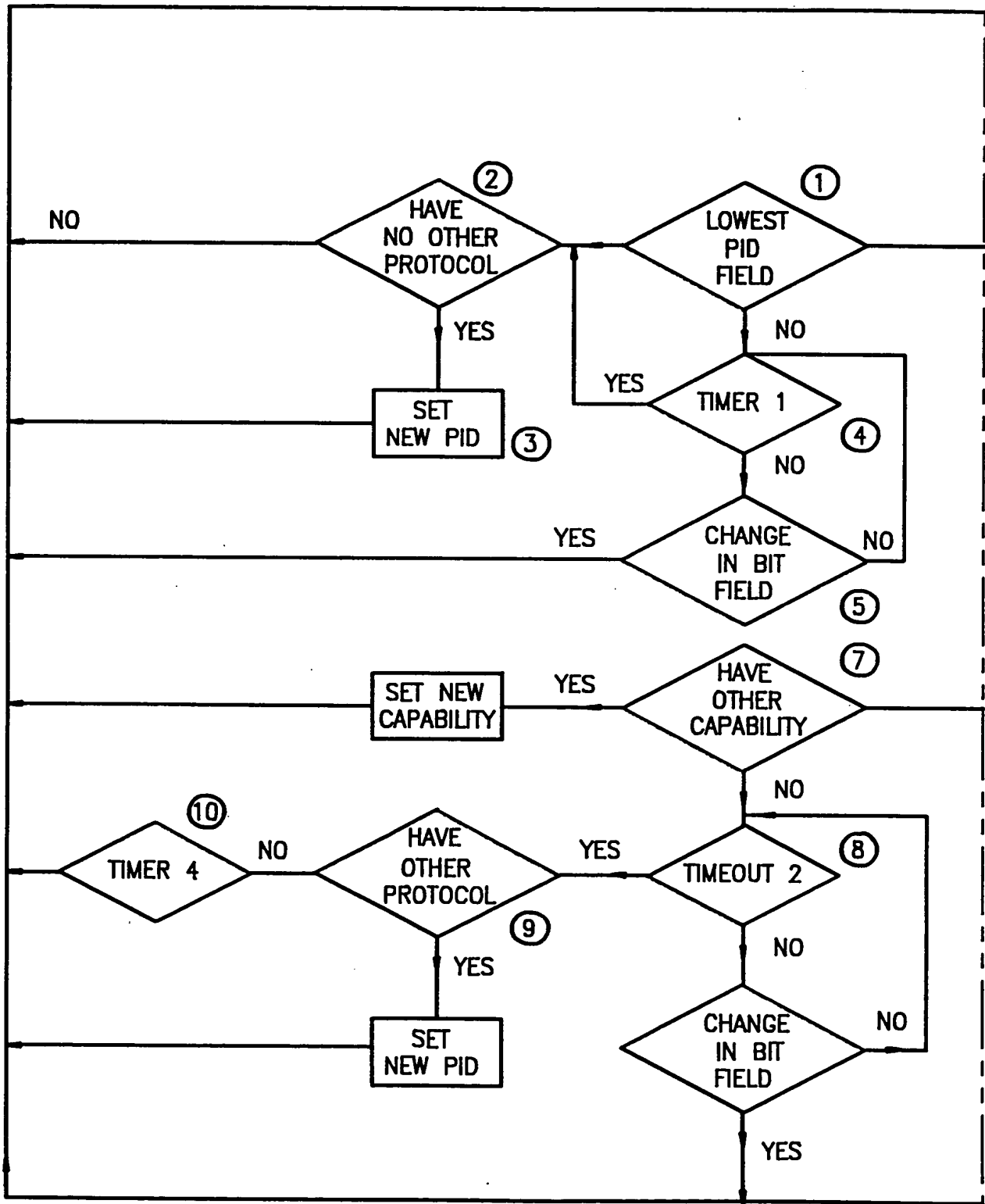


FIG. 23A

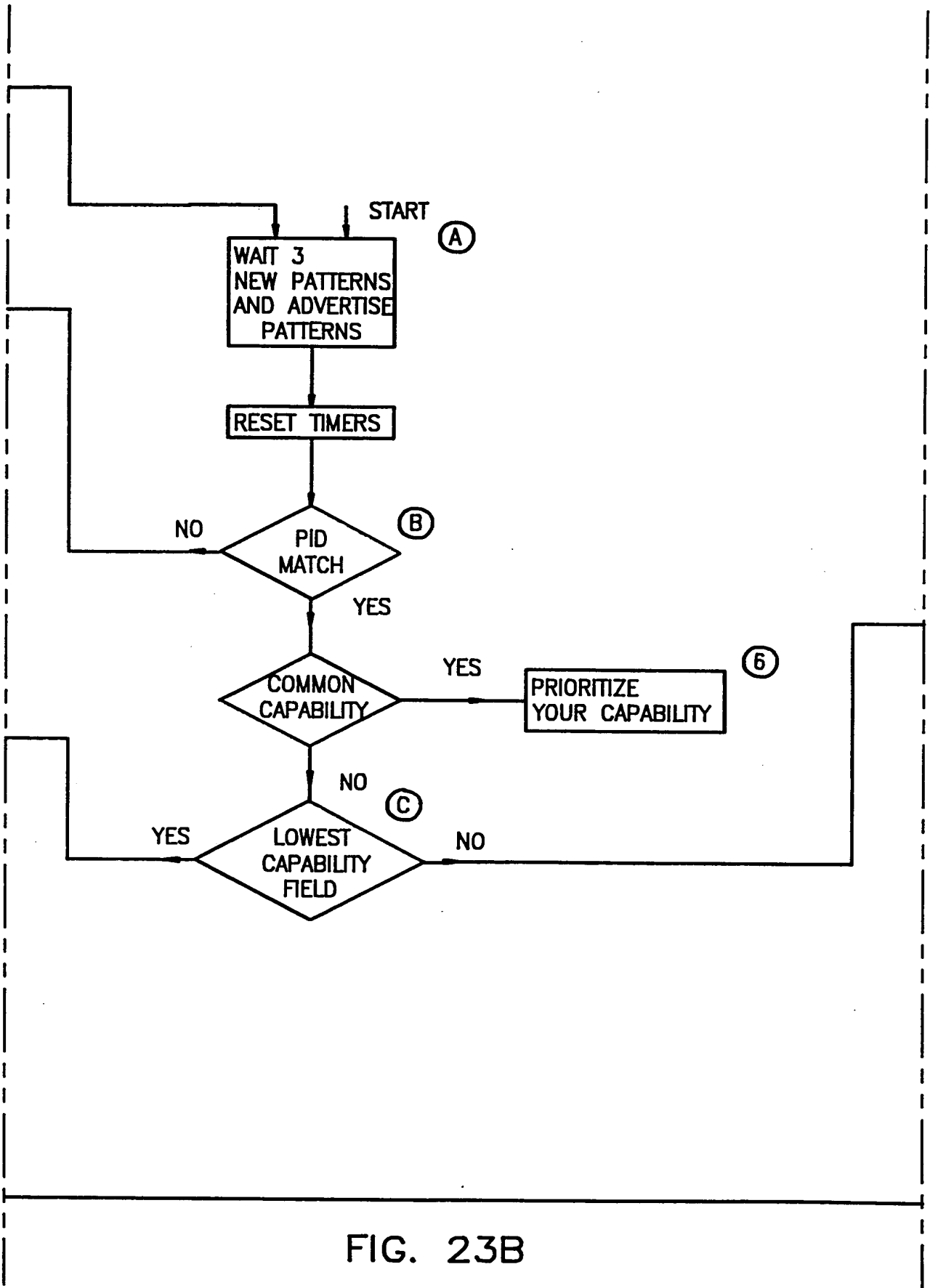


FIG. 23B

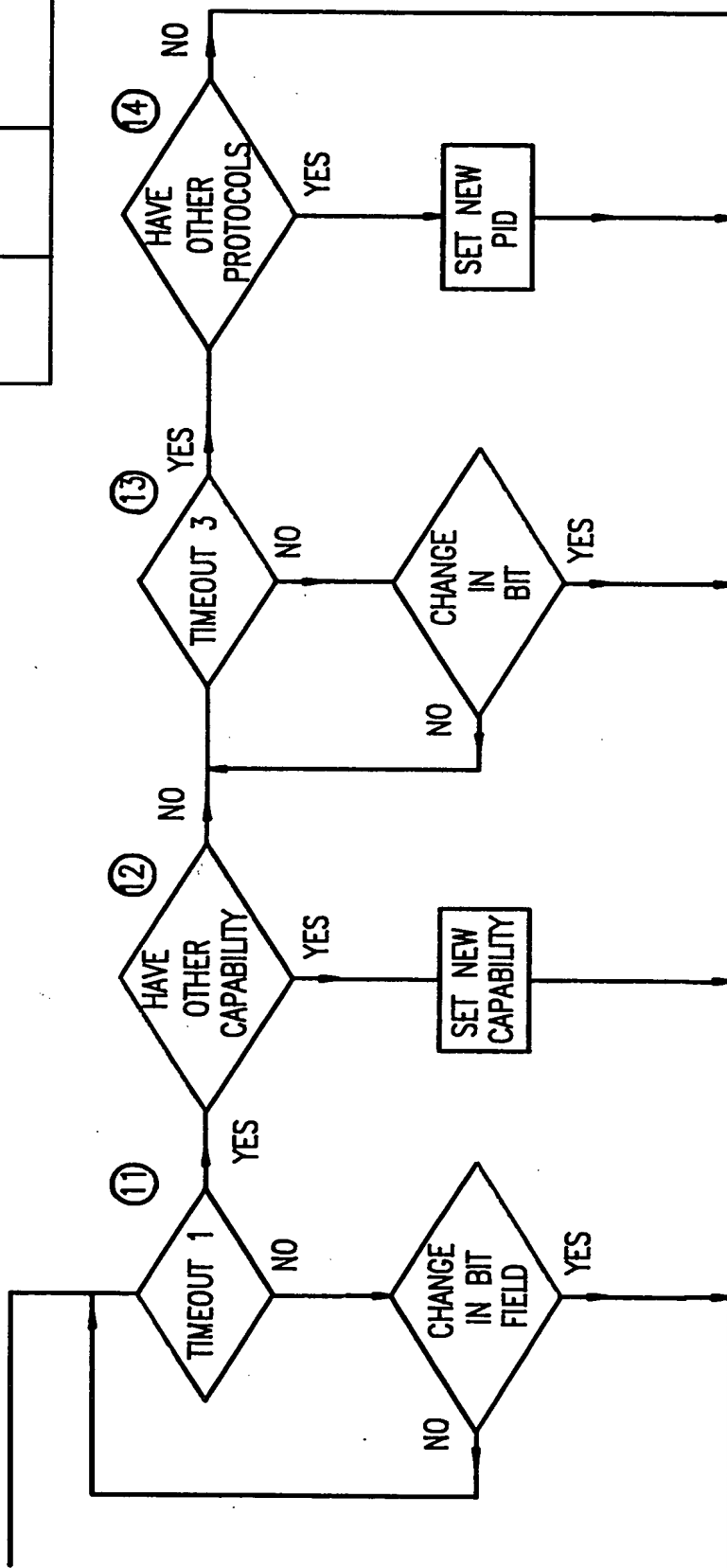
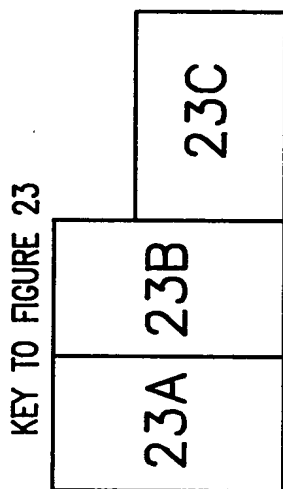


FIG. 23C

Data Word Encoding (Multi-Protocol Negotiation)

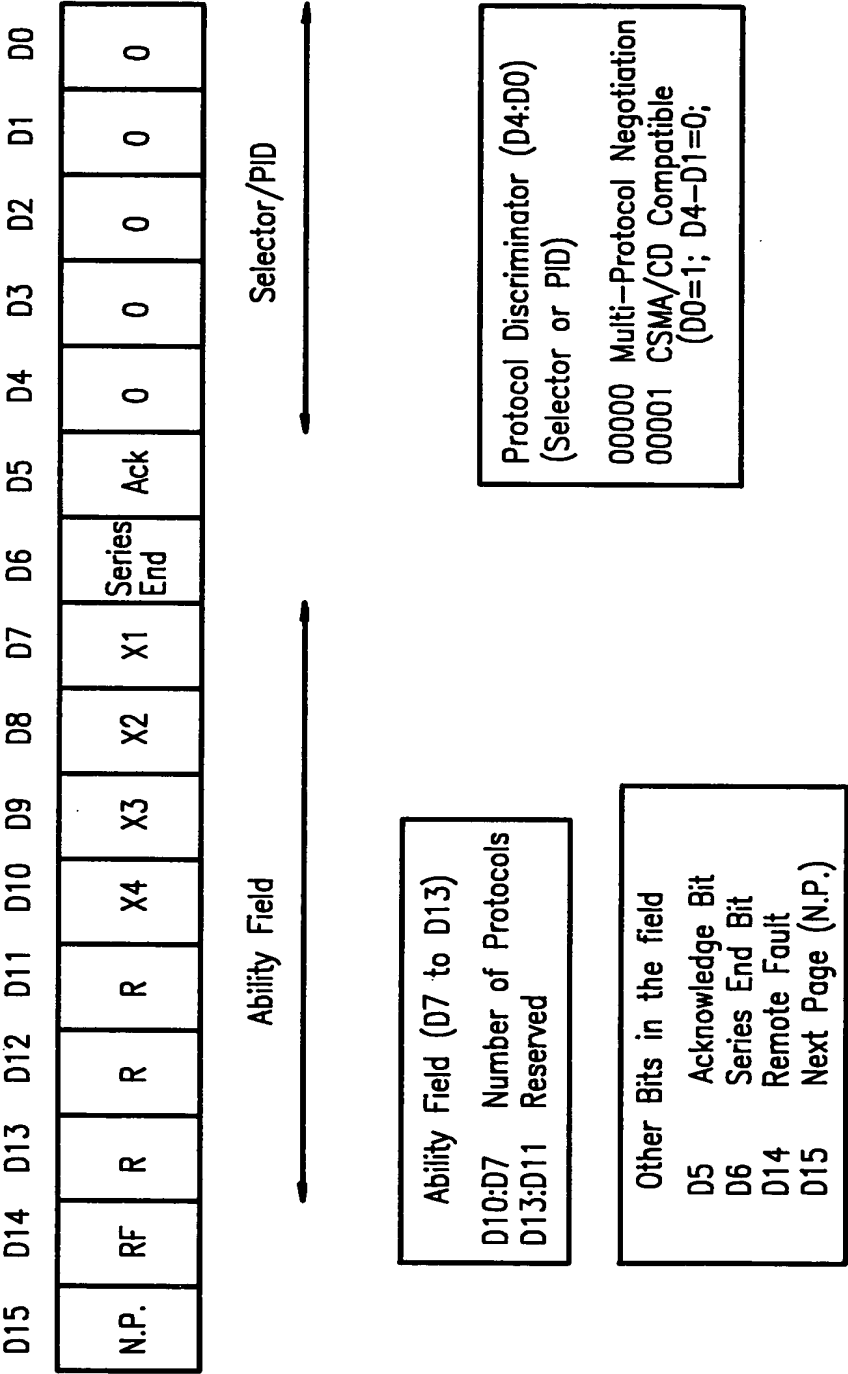


FIG. 24

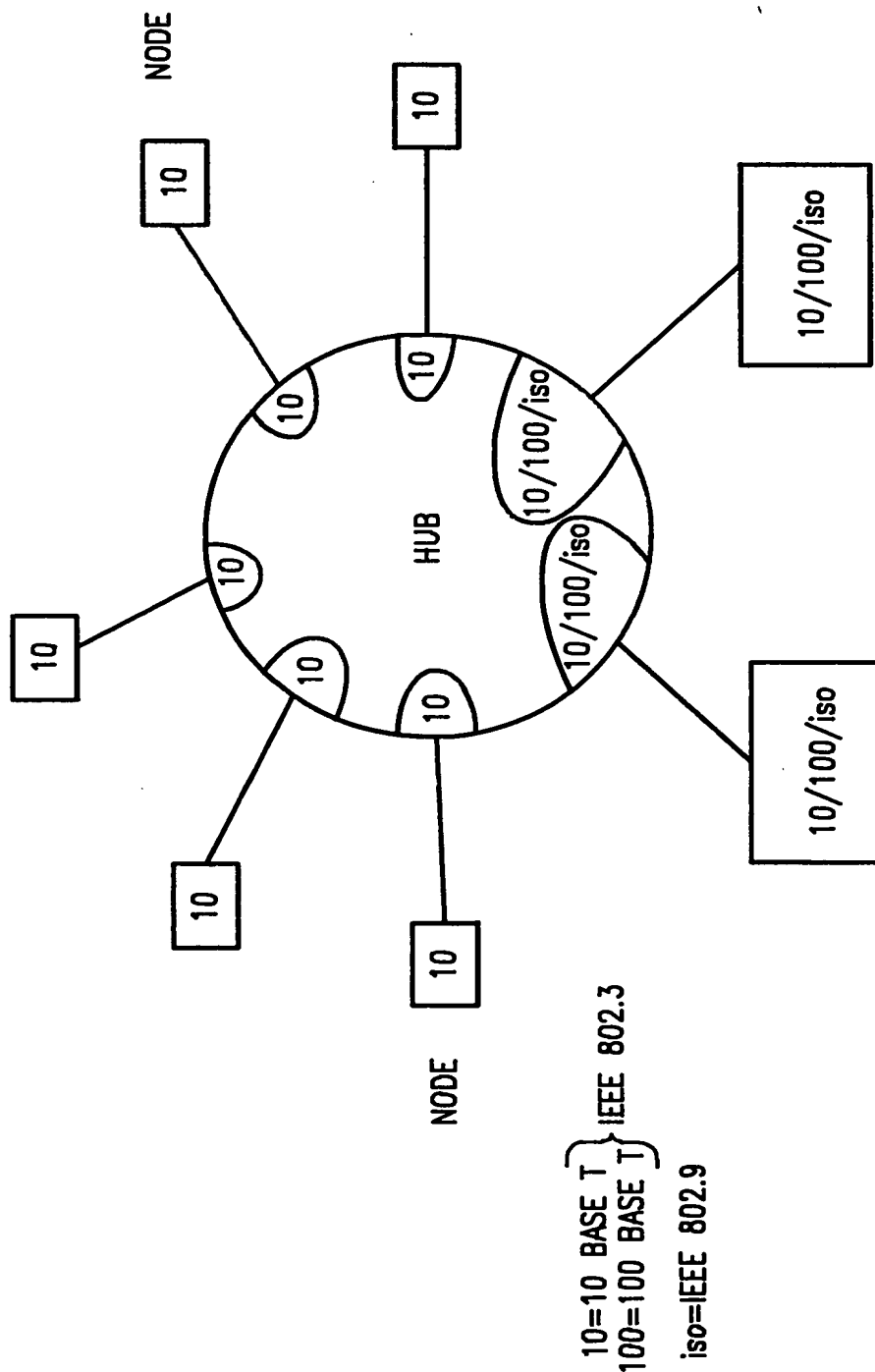


FIG. 25